

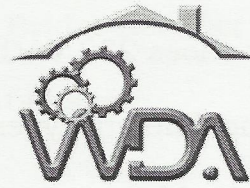
ETL - Power Electronics

T078

Thursday, 31/10/2013

8:30 -11:30 AM

WORKFORCE DEVELOPMENT AUTHORITY



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**ADVANCED LEVEL NATIONAL EXAMINATIONS, 2013;
TECHNICAL AND PROFESSIONAL TRADES**

EXAM TITLE: Power Electronics

OPTION: Electronics and Telecommunication (ETL)

DURATION: 3hours

INSTRUCTIONS:

The paper is contains Three (2) Sections:

Section I: Sixteen (16) questions, all **Compulsory**. **55marks**

Section II: Five (5) questions, **Choose any Three (3)**. **30marks**

Section III: Two (2) questions, **Choose any One (1)**. **15marks**

Section I: All the 16 questions are Compulsory**55marks**

01. Explain why is IGBT a voltage controlled device. **1mark**
02. A voltmeter shoes 230 VAC. What does it mean? **1mark**
03. What are the different types of power MOSFET? **2marks**
04. Differentiate latching current from holding current in case of a thyristor. **2marks**
05. What are the main components used for isolating the Power Circuits, Power Semiconductor from the low-power circuit? **2marks**
06. Describe a snubber circuit and precise its function. **2marks**
07. What are the different types of chopper with respect to commutation process? **3marks**
08. Differentiate the structure of a DIAC from that of a bipolar Transistor. **3marks**
09. What are the different methods of firing circuits for line commutated converter? **3marks**
10. How is the inverter circuit classified based on commutation circuitry? **4marks**
11. Identify different losses that occur in a thyristor during working conditions. **4marks**
12. Identify five (5) among different applications of controlled rectifier. **5marks**
13. Describe a dc chopper and identify its main characteristics. **5marks**
14. What is the difference between power diode and signal diode? **6marks**
15. Identify in the following components which are current driven and those which are voltage driven: SCR; GTO; GTR; MCT; IGCT; SIT. **6marks**
16. Explain why GTOs are more used in power converters compared to SCRs. **6marks**

Section II. Choose and Answer any three questions. 30marks

17. A UJT has 10V between its bases and is characterized by intrinsic stand-off ratio of 0.6; inter-base resistance of 10k Ω . The forward voltage drop in the pn junction is 0.7V. Determine the value of R_{B1} and R_{B2} ; the standoff voltage and the peak-point voltage. **10marks**
18. An operational amplifier has feedback resistor $R_f = 12K \Omega$ and the resistances in the input sides are $R_1=12K \Omega$, $R_2=2K \Omega$ and $R_3=3K \Omega$ with the corresponding inputs voltages applied on inverting terminal $V_{i1} = +9V$, $V_{i2} = -3V$ and $V_{i3} = -1V$. Non-inverting terminal is grounded. Calculate the output voltage. **10marks**

19. Identify different types of filters commonly used in power supply circuits and give corresponding schematics. **10marks**

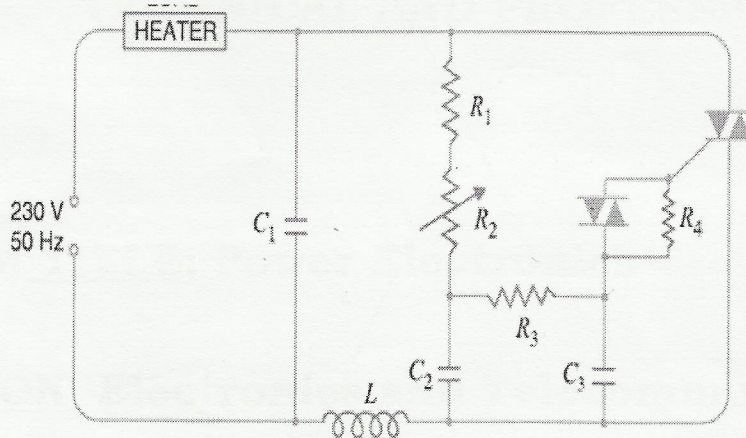
20. a) Draw the circuit diagram of Four-diode Full-wave Bridge Rectifier and show the direction of the current through different diodes. **4marks**

b) Identify its advantages and disadvantages. **6marks**

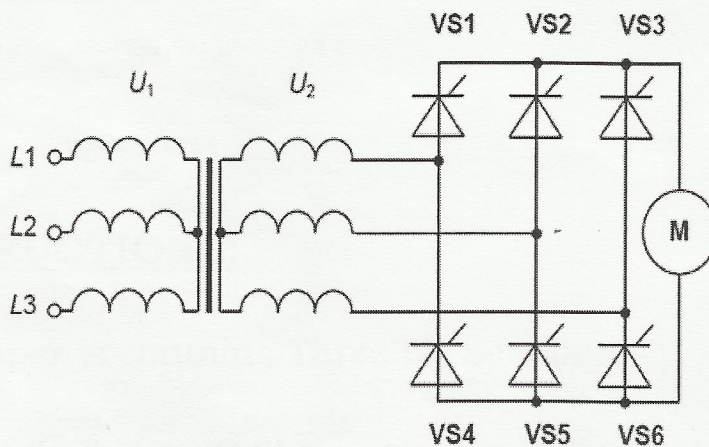
21. Draw the circuit of an OPAMP non-inverting voltage feedback amplifier and deduce the equation for its closed-loop gain. **10marks**

Section III. Choose and answer any One (1) question 15marks

22. Describe the behavior the following circuit : **15marks**



23. Analyze the circuit represented bellow and answer to the questions :



a) What is the type of that circuit? **1mark**

b) Identify its main characteristics. **6marks**

c) Determine the switching order of the elements represented by VS1, VS2, ..., VS6. **6marks**

d) What will happen if the element M possesses high inductance? **2marks**

SECTION I.

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Q1. IGBT is voltage controlled because :

- the controlling parameter is gate-emitter voltage. 1
 - the gate terminal is insulated. 1
 - the output current is controlled by input voltage. 1
- Consider one only

1 mark

Q2. 230 VAC shown by voltmeter means that :

- It is the rms (root mean square) value. 1
 - It is the effective value of AC voltage. 1
- Consider one only

2 marks

Q3. Types of power MOSFET

- Enhancement Only MOSFET $\begin{cases} \text{N-channel MOSFET} \\ \text{P-channel MOSFET} \end{cases}$ 1
 - Depletion-Enhancement only MOSFET $\begin{cases} \text{N-channel MOSFET} \\ \text{P-channel MOSFET} \end{cases}$ 1
- Consider two

2 marks

Q4. Latching current is the minimum value of anode current which it must attain during turn on process to maintain conduction when gate signal is removed. 1

2 marks

- The Holding current is defined as the minimum value of anode current below which it must fall to for turning off the thyristor. 1

Consider two only

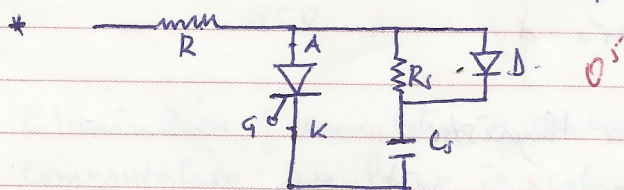
- Holding current is the maximum anode current, gate being open, at which SCR is turned-off from ON condition. 1

Q5. Components used isolating power circuits :

- opto-couplers (like opto-triac, opto-thyristor, Relays) ~~solid state relay~~
 - transformers
 - Solid state relay
- Consider two only

2 marks

Q6. Snubber circuit and its application :



2 marks

* It consists of a series combination of a resistor and a capacitor in parallel with the thyristor. 1

* It is mainly used for $\frac{dv}{dt}$ protection. 1

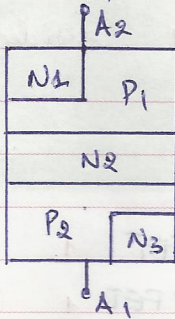
07. Types of chopper w.r.t. to commutation process.

- voltage commutated chopper 1
- current commutated chopper 1
- load commutated chopper 1
- line commutated chopper 1
- Forced commutated chopper 1

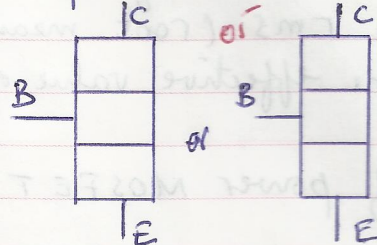
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3 marks

08. DIAC structure



Bipolar transistor.



- It has four junctions
- It has two terminals
- There is no terminal attached to base
- Three regions are identical ^{layer} inside
- The doping concentration are identical to give the device symmetrical properties.

- It has ~~two~~ three junctions
 - It has three terminals
 - All terminals are attached to
 - The regions are not identical ^{base}
- base only three consider*

3 marks

09. Methods of firing circuits for line commutated converter :

- UJT firing circuit 1
- The cosine wave crossing pulse timing control 1
- Digital firing schemes 1

3 marks

10. Classification of inverters based on commutation circuitry :

- line commutated inverters 1
- load commutated inverters 1
- Self-commutated inverters 1
- Forced commutated inverters 1

4 marks

11. Different losses occurring in thyristor :

- Forward conduction losses 1
- loss due to leakage current during forward and reverse blocking 1
- Switching losses at turn-on and turn-off 1
- Gate triggering loss 1

4 marks

12. Five applications of controlled Rectifier.

- Steel rolling mills, printing process, textile mills (different machines) employing DC motor drives 1
- DC traction 1
- Electro-chemical and electro-metallurgical process 1
- Portable hand tool drives 1
- Magnet power supplies 1
- HVDC transmission system. 1
- Rectification 1

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5 marks.

13. * A DC chopper is a high speed static switch used to obtain variable DC voltage from a constant DC voltage. 1

DC chopper characteristics:

- * High efficiency 1
- * Smooth acceleration 1
- * Fast dynamic response 1
- * Regeneration 1

5 marks.

14. Difference between power diode and signal diode.

| Power diode | Signal diode. |
|---|---|
| - Constructed with n-layer, called drift region between p ⁺ layer and n ⁺ layer 1 | - Drift region is not present 1 |
| - The voltage, current and power ratings are higher 1 | - Voltage, current, and power are lower 1 |
| - Power diodes operate at high speeds 1 | - Operates at higher switching speed 1 |
| - lower switching speed | - operate at low speed |

6 marks.

15. Current driven devices

SCR 1
GTO 1
GTR 1

Voltage controlled devices.

MeT 1
IGCT 1
SIT 1

6 marks.

16. a) Elimination of commutation of commutating components in forced commutation resulting in reduction in cost, weight and volume. 1

b) Reduction in acoustic noise and electromagnetic noise due to elimination of commutation chokes. 1

c) Faster turn-off, permitting high switching frequencies. 1

d) Improved efficiency of the converters. 1

6 marks.

SECTION II.

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17. a) Determine the value of R_{B1} and R_{B2} .
 $V_{BB} = 10V$, $\eta = 0.6$, $R_{BB} = 10k\Omega$, $V_D = 0.7V$

$$R_{BB} = R_{B1} + R_{B2} \quad 1$$

$$10 = R_{B1} + R_{B2} \quad 1$$

$$\eta = \frac{R_{B1}}{R_{B1} + R_{B2}} \quad 1$$

$$0.6 = \frac{R_{B1}}{10} \quad 1$$

6 marks

$$R_{B1} = 0.6 \times 10k\Omega = 6k\Omega \quad 1$$

$$R_{B2} = 10 - R_{B1} = 10 - 6 = 4k\Omega \quad 1$$

10 marks.

- b) Standoff voltage

$$V_{\text{standoff}} = \eta \times V_{BB} \quad 1$$

$$= 0.6 \times 10 = 6V \quad 1$$

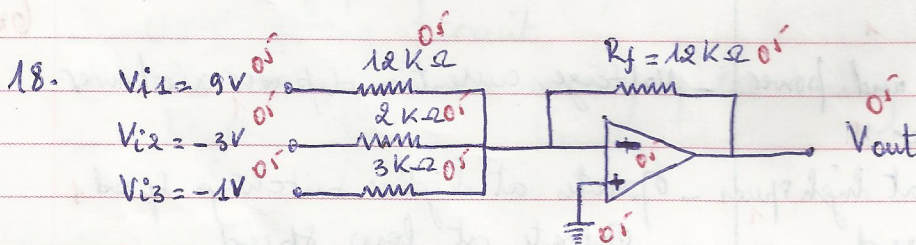
2 marks

- c) Peak-point voltage

$$V_{\text{peak-point}} = \eta \times V_{BB} + V_D \quad 1$$

$$= 6 + 0.7 = 6.7V \quad 1$$

2 marks



$$V_{\text{out}} = - \left(\frac{R_f}{R_1} V_1 + \frac{R_f}{R_2} V_2 + \frac{R_f}{R_3} V_3 \right) \quad 1$$

$$= - \left(\frac{12}{12} (9) + \frac{12}{2} (-3) + \frac{12}{3} (-1) \right) \quad 1$$

5 marks.

$$= - (9 - 18 - 4) \quad 1$$

$$= -9 + 18 + 4 \quad 1$$

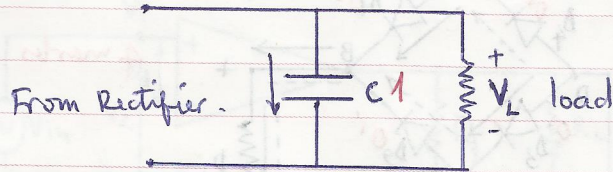
$$= 13V \quad 1$$

10 marks

19. Types of filters.

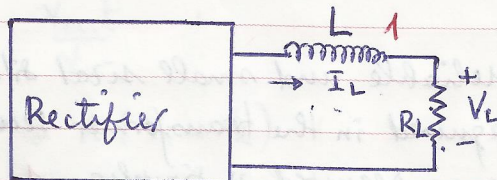
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(i) capacitor : c filter 1



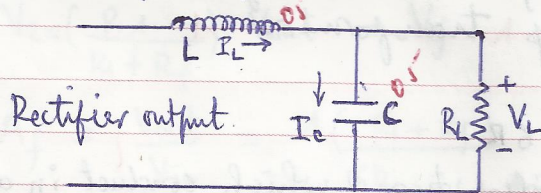
2 marks

(ii) Series inductor filter : L filter 1



2 marks

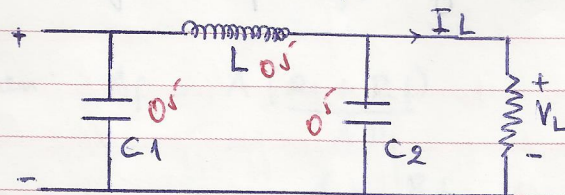
(iii) choke input filter : LC filter 1



2 marks

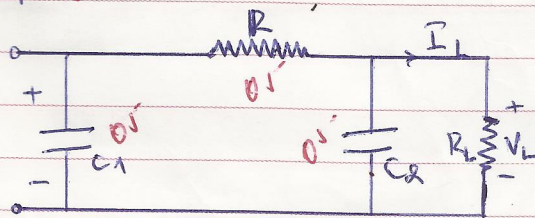
10 marks

(iv) Π Filter : c L c filter 0.5



2 marks

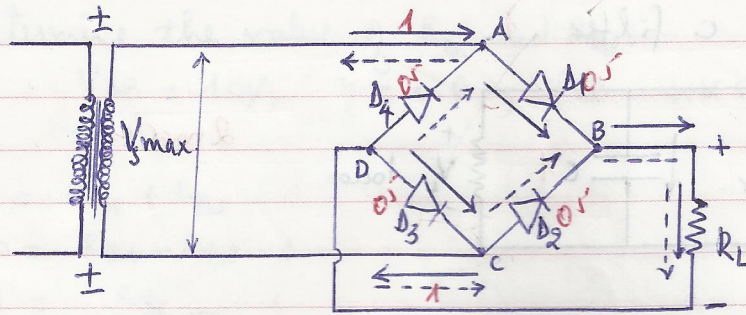
(v) RC filter 0.5



2 marks

Qo. a) Four diode full wave bridge rectifier. (FWBR)

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b) * Advantages of FWBR.

1. Low cost, highly reliable and small sized silicon diodes 1
2. No centre tap is required in the transformer secondary; the transformer required is simpler. 1
3. The PIV is one half that of centre-tap rectifier 1
4. Transformer Utilization factor (TUF) is higher than that of centre tap transformer. 1

10 marks

* Disadvantages of FWBR.

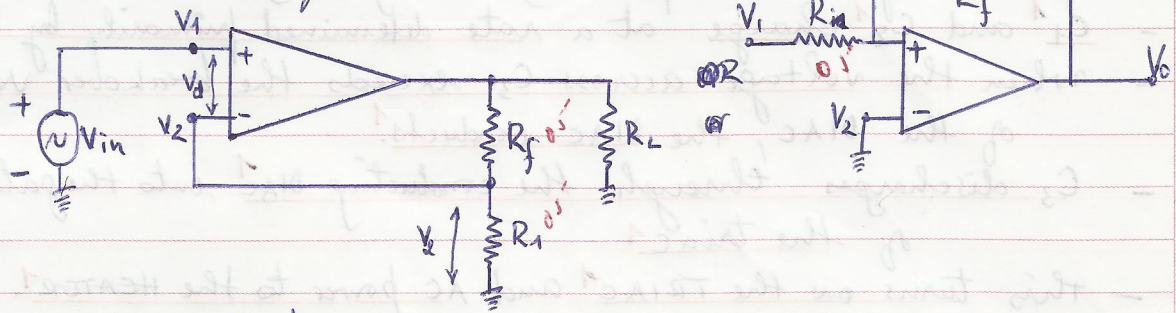
1. It needs four diodes, two of which conduct in alternate half cycles 1
2. Total voltage drop in diodes becomes double of that in case of centre tap rectifier. 1

2 marks

21. OP-AMP non-inverting voltage feedback amplifier.

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Schematic diagram.



$$A_f = \frac{V_{out}}{V_{in}} \quad 1$$

$$V_{out} = A(V_1 - V_2) \quad 1$$

$$V_1 = V_{in} \quad 1$$

$$V_2 = \left(\frac{R_1}{R_1 + R_f} \right) \cdot V_{out} = V_f \quad 1$$

$$A_f = \frac{V_{out}}{V_{in}} = \frac{A(R_1 + R_f)}{(R_1 + R_f + AR_1)} \quad 1$$

As $AR_1 \gg (R_1 + R_f)$ and $R_1 + R_f + AR_1 \approx AR_1 \quad 2$

Then $A_f = \frac{A(R_1 + R_f)}{AR_1} \quad 1$

$$= 1 + \frac{R_f}{R_1} \quad 1$$

Remarks.

SECTION III.

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22. - Input voltage increase positively¹ or negatively¹
- C_1 ¹ and C_2 ¹ charge at a rate determined primarily by R_2 ¹
 - when the voltage across C_3 exceeds the breakover voltage¹ of the DIAC, the DIAC conducts¹.
 - C_3 discharges through the conducting DIAC¹ into the gate of the triac¹
 - this turns on the TRIAC¹ and AC power to the HEATER¹.
 - By adjusting the value of R_2 ¹, any portion of positive or negative half cycles of ~~ac~~ supply voltage can be passed through the heater¹.
 - this permits a smooth control¹ of the heat output from heater¹.

15 mark

23. a) It is a three-phase bridge rectifier using 6 thyristors¹ or it is a 3 ϕ controlled bridge rectifier.

b) 1 - This circuit does not require neutral line of the 3-phases¹ source

2 - It is a 2 quadrant rectifier¹

3 - There is more than one open thyristor in cathode group or anode group¹

4 - Voltage ripple is low because the output voltage ⁴ consists of 6 pulses per period ²

5 - At least one thyristor from each group must conduct to facilitate the flow of the motor current¹

6 - ~~Determine the switching order of the elements represented~~

15 marks

c) The switching order :

1) $V_{S1} + V_{S6}$ ¹

2) $V_{S6} + V_{S2}$ ¹

3) $V_{S2} + V_{S4}$ ¹

4) $V_{S4} + V_{S3}$ ¹

5) $V_{S3} + V_{S5}$ ¹

6) $V_{S5} + V_{S1}$ ¹

d) If the element M (which constitutes a load to the circuit: for example a motor) possesses high inductance, the forward current continues to flow on the negative anode voltage¹ and the switching-off of the thyristor is delayed.¹